

PATENT

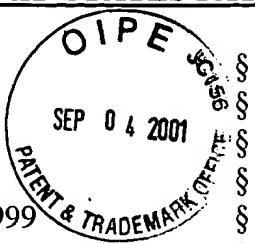
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor: Sistare, et al.

Serial No.: 09/303,464

Filing Date: April 30, 1999

For: System and Method for  
Controlling Co-Scheduling of  
Processes of Parallel Program



Atty. Dkt. No.: 5181-93900

I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on the date indicated below:

B. Noel Kivlin  
Name of Registered Representative

8-31-01  
Date

Signature

POWER OF ATTORNEY BY ASSIGNEE  
AND REVOCATION OF PREVIOUS POWERS

Commissioner for Patents  
Washington, D.C. 20231

Sir:

SUN MICROSYSTEMS, INC., ("assignee"), a Delaware corporation having a place of business at 901 San Antonio Road, Palo Alto, California 94303, certifies that to the best of assignee's knowledge and belief it is the assignee of the entire right, title, and interest in and to the above-referenced patent and represents that the undersigned is a representative authorized and empowered to sign on behalf of the assignee.

Assignee has reviewed the assignment documents that evidence the placement of title in the assignee, true and correct copies of which are attached hereto, and understands and believes that these assignment documents have been submitted for recordation in the U.S. Patent and Trademark Office.

Pursuant to 37 C.F.R. §§1.36 and 3.71, the assignee hereby revokes all powers of attorney previously given and appoints

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Andrew C. Chen	Reg. No. 43,544
Jeffrey L. Myers	Reg. No. 44,252

*each of said attorneys being employed by Sun Microsystems; and*

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*each of said attorneys or agents being a member or an associate of the firm of Conley, Rose & Tayon, P.C., as attorney or agent for so long as they remain with such company or firm, with full power of substitution and revocation, to prosecute the application, to make alterations and amendments therein, to transact all business in the Patent and Trademark Office in connection therewith, and to receive the Letters Patent.*

Pursuant to 37 C.F.R. §3.71, the assignee hereby states that prosecution of the above-referenced patent application is to be conducted to the exclusion of the inventor(s).

Send all future correspondence to: B. Noël Kivlin  
Conley, Rose & Tayon, P.C.  
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Assignee of Interest

Sun Microsystems, Inc.  
901 San Antonio Road  
PAL 01-521  
Palo Alto, CA 94303

Dated: August 16, 2001

By: 

Name: Kenneth Olsen  
Reg. No.: 26,493  
Title: Vice President, Intellectual Property

## ASSIGNMENT

THIS ASSIGNMENT, effective as of September 15, 1995, of certain Patent Properties of Kendall Square Research Corporation ("KSR"), said Patent Properties to include all U. S. Letters Patent and Patent Applications of KSR, including but not limited to those Letters Patent and Patent Applications identified on the attached Exhibit, along with all divisional, renewal, substitute, continuation and all other applications based in whole or in part upon any said Letters Patent or Patent Applications and/or from which said Letters Patent or Patent Applications claim priority, and any invention or discovery described therein as well as any patent which issues therefrom, any reissues and extensions of any of the foregoing, and every priority right that is or may be predicated upon or arise therefrom (identified herein in the singular as a "Patent Property" and in the plural as "Patent Properties");

WHEREAS, KSR, which has a place of business at 170 Tracer Lane, Waltham, Massachusetts 02154, is owner of all right, title and interest in and to the Patent Properties; and

WHEREAS, Sun Microsystems, Inc., ("SMI") a Delaware Corporation, which has a place of business at 2550 Garcia Avenue, Mountain View, California 94043, is desirous of acquiring the entire right, title and interest in and to the Patent Properties;

NOW, THEREFORE, for good and valuable consideration, receipt and sufficiency of which is hereby acknowledged, KSR does hereby sell, assign and transfer unto the said SMI, all rights, title and interest in and to the Patent Properties, the same to be held and enjoyed by the said SMI for its own use and behoof, and for its legal representatives and assigns, to the full end of the term for which each said Patent Property is granted, as fully and entirely as the same would have been held by KSR had this assignment not been made, this assignment to include all claims for damages by reason of past infringement of any said Patent Property, along with the right to sue in its own name for and collect same for SMI's own use. KSR further agrees to perform such lawful acts and to execute such further applications, assignments and other lawful documents as SMI may reasonably request to fully effectuate this Assignment.

IN WITNESS WHEREOF, I set my hand and seal hereto at Wellesley, Massachusetts  
this 15 day of February, 1996

Kendall Square Research Corporation

By:

Stacey M. Cannon  
Stacey M. Cannon  
Corporate Secretary

State of Massachusetts )  
                          )  
                          ) ss.  
County of Middlesex   )

On this 15 day of February, 1996, before me appeared Stacey M. Cannon, to  
me known and known to me to be the person described in and who executed the foregoing  
instrument, and who acknowledged the same to be her free act and deed.

Richard Jordan

Notary Public

RICHARD JORDAN

NOTARY PUBLIC

MY COMMISSION EXPIRES JUNE 20, 1997

[SEAL]

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Patent Property Assignment  
Kendall Square Research Corporation to  
Sun Microsystems, Inc.  
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**EXHIBIT**

**Patent Property Assignment**  
Kendall Square Research Corporation to  
Sun Microsystems, Inc.

**U. S. Patent No. 5,055,999**  
Issued October 8, 1991  
Title: Multiprocessor Digital Data Processing System

**U. S. Patent No. Des. 324,376**  
Issued March 3, 1992  
Title: Modular Computer Assembly (Design)

**U. S. Patent No. 4,934,764**  
Issued June 19, 1990  
Title: Computer System Module Assembly

**U. S. Patent No. 5,251,308**  
Issued October 5, 1993  
Title: Improved Multiprocessor System

**U. S. Patent No. 5,297,265**  
Issued March 22, 1994  
Title: Shared Memory Multiprocessor System And Method Of Operation Thereof

**U. S. Patent No. 5,335,325**  
Issued August 2, 1994  
Title: High Speed Switching Apparatus And Method

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**Exhibit**

**Patent Property Assignment**  
Kendall Square Research Corporation to  
Sun Microsystems, Inc.

**Page 1 of 4**

U. S. Patent No. 5,282,201  
Issued January 25, 1994  
Title: Dynamic Packet Routing Network

U. S. Patent No. 5,226,039  
Issued July 6, 1993  
Title: Packet Routing Switch

U. S. Patent No. 5,341,483  
Issued August 23, 1994  
Title: Dynamic Hierarchical Associative Memory

U. S. Patent No. Des. 338,663  
Issued August 24, 1993  
Title: Cap For Stacks Of Modular Computer Units

U. S. Patent No. 5,119,481  
Issued June 2, 1992  
Title: Multiprocessor System With Shift Register Bus

U. S. Patent No. Des. 343,828  
Issued February 1, 1994  
Title: Computer Cable Through

U. S. Patent No. 5,313,647  
Issued May 17, 1994  
Title: Digital Data Processor With Improved Checkpointing And Forking

U. S. Patent No. 5,245,563  
Issued September 14, 1993  
Title: Control Signal Method And Device

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Exhibit

Patent Property Assignment  
Kendall Square Research Corporation to  
Sun Microsystems, Inc.  
Page 2 of 4

U. S. Patent Application Ser. No. 08/189,497

Filed December 31, 1994

Title: Improved Method And Apparatus For Data Access In Multiprocessor Digital Data Processing System

U. S. Patent Application Ser. No. 08/360,050

Filed December 20, 1994

Title: Method And Apparatus For Timing Control

U. S. Patent Application Ser. No. 08/403,308

Filed March 14, 1995

Title: Multiprocessor Digital Data Processing System/Shared Memory Multiprocessor System And Method Of Operation

U. S. Patent Application Ser. No. 08/461,167

Filed June 5, 1995

Title: Improved Digital Data Processor With Distributed Memory System

U. S. Patent Application Ser. No. 08/461,174

Filed June 5, 1995

Title: Digital Data Processor With Improved Paging

U. S. Patent Application Ser. No. 08/461,166

Filed June 5, 1995

Title: Multiprocessor System With Multiple Instructions

U. S. Patent Application Ser. No. 08/462,759

Filed June 5, 1995

Title: Fault Containment System For Multiprocessor With Shared Memory

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Exhibit

Patent Property Assignment

Kendall Square Research Corporation to

Sun Microsystems, Inc.

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U. S. Patent Application Ser. No. 08/461,178

Filed June 5, 1995

Title: Parallel Processing Apparatus And Method For Utilizing Tiling

PCT Application Ser. No. PCT/US95/01356

Filed January 31, 1995

Title: Improved Method And Apparatus For Data Access In Multiprocessor Digital Data Processing System

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Exhibit

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Kendall Square Research Corporation to  
Sun Microsystems, Inc.

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